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(54) Title: A METHOD AND AN ARRANGEMENT FOR DESPREADING A CODED SEQUENCE IN A COMMUNICATION SYSTEM			
(57) Abstract			
<p>The invention relates to a method and an arrangement for implementing a bank of correlators, corresponding to a set of Orthogonal Gold codes, in a receiver used in a Direct Sequence Code Division Multiple Access system (DS-CDMA). The bank of correlators generates N correlation values (<math>CV_0, \dots, CV_{N-1}</math>) from N received symbols (<math>s_0, s_1, \dots, s_{N-1}</math>) in a sequence. The received symbol sequence (<math>s_0, s_1, \dots, s_{N-1}</math>), where each symbol has been spread by an Orthogonal Gold code respectively, is buffered (401). The buffered symbol sequence is multiplied (402) element-by-element with an arbitrary sequence (<math>a_0, a_1, \dots, a_{N-1}</math>), in order to obtain a set of products (<math>s_0 * a_0, s_1 * a_1, \dots, s_{N-1} * a_{N-1}</math>). Said arbitrary sequence is the same sequence used when generating the Orthogonal Gold Codes. The set of products (<math>s_0 * a_0, s_1 * a_1, \dots, s_{N-1} * a_{N-1}</math>) is then permuted (403) in order to obtain a sequence, which is equal to a sum of a number of sequences from a set of Walsh sequences. The sequence is then transformed (404) with Fast Hadamard Transform as to obtain said correlation values (<math>CV_0, \dots, CV_{N-1}</math>).</p>		<pre> graph TD     401[buffering] --&gt; 402[multiplication]     402 --&gt; 403[permutation]     403 --&gt; 404[Fast Hadamard Transformation]           </pre>	

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A METHOD AND AN ARRANGEMENT FOR DESPREADING A CODED SEQUENCE IN A COMMUNICATION SYSTEM

TECHNICAL FIELD

The present invention relates generally to a method and an arrangement for despreading of a received signal in a multicode DS-CDMA system and more particularly it relates to a method and an arrangement for implementing a bank of correlators, corresponding to a set of Orthogonal Gold codes or alike, in a receiver used in a Direct Sequence Code Division Multiple Access system (DS-CDMA).

10 DESCRIPTION OF THE BACKGROUND ART

A spread spectrum communication system employing a direct sequence method, as for example DS-CDMA (Direct Sequence Code Division Multiple Access) system, utilises a method of generating from an information signal, a transmission signal, where the transmission signal have a significantly wide bandwidth with respect to the original information signal. This is a result of spreading the information signal by a high rate spreading sequence such as for example a high bit rate pseudo noise (PN) sequence. The digitised informational signal and the high rate spreading sequence are typically multiplied together. This combination is called coding or spreading the informational signal. A plurality of coded information signals are transmitted on a same radio frequency carrier wave and jointly received as a composite signal at a receiver. Each of the coded signals overlaps all of the other coded signals, as well as noise-related signals, in both frequency and time. By correlating the composite signal with one of the unique spreading sequences, the corresponding information signal can be isolated and decoded. Each information signal is thus allocated a unique spreading

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code and a receiver uses the same spreading code as that used in the transmitter to perform a despread operation.

Multicode transmission scheme is a method to provide high-rate and variable-rate data services in DS-CDMA systems without decreasing the processing gain or increasing the spreading bandwidth. In such a scheme several code channels are assigned to a single user. High-rate data stream is split into a number of parallel low-rate streams. These low-rate streams are spread by different sequences and added together before power amplification.

To avoid interference between different code channels of a single user, all spreading sequences (codes) should be orthogonal within data symbol duration. To make the other-user interference as a noise-like interference, each single users code channels are further multiplied by a user specific pseudorandom spreading sequence. The number of used orthogonal codes depends on the current data rate. The receiver will have an easier task if it knows which spreading codes are currently used, or at least knows the number of code channels.

An example of orthogonal sequences is a so-called Orthogonal Gold sequences. A set ( $\hat{G}$ ) of Orthogonal Gold codes ( $OG_0, OG_1, \dots, OG_{N-1}$ ) with elements belonging to an alphabet of 0 and 1, is defined as a matrix obtained from a modulo-two summation (XOR) between each row of an orthogonal matrix ( $\hat{M}$ ) and a sequence ( $\hat{a}$ ), i.e.

$$\hat{G} = \begin{bmatrix} OG_0 \\ OG_1 \\ \dots \\ OG_{N-1} \end{bmatrix} = \begin{bmatrix} \hat{a} \\ \hat{x}_1 \oplus \hat{a} \\ \dots \\ \hat{x}_{N-1} \oplus \hat{a} \end{bmatrix} = \hat{M} \oplus \hat{a}$$

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where

$$\hat{M} = \begin{bmatrix} 0 & 0 & \dots & 0 & 0 & 0 \\ x_1 & x_2 & x_3 & \dots & x_{N-1} & 0 \\ x_2 & x_3 & x_4 & \dots & x_1 & 0 \\ \dots & \dots & \dots & \dots & \dots & \dots \\ x_{N-1} & x_1 & x_2 & \dots & x_{N-2} & 0 \end{bmatrix}$$

and

$$\hat{a} = [a_1 \ a_2 \ \dots \ a_{N-1} \ 0].$$

- 5 The orthogonal matrix ( $\hat{M}$ ) is obtained from another matrix ( $M$ ) consisting of all cyclic shifts of a first maximum length PN sequence (m-sequence)  $\{x_k\}$ , where  $k=1,2,\dots,N-1$ , by bordering the matrix ( $M$ ) on the top with a row of zeros and on the right side by a column of zeros.
- 10 The sequence ( $\hat{a}$ ) is another m-sequence  $\{a_k\}$ , where  $k=1,2,\dots,N-1$ , with a zero added on the right end. The sequences  $\{x_k\}$  and  $\{a_k\}$  should form a so-called preferred pair of m-sequences to obtain optimum periodic cross-correlation function. Optimum cross-correlation function is one which has minimum maximum
- 15 value for a given sequence length and a given number of sequences.

However, the actual definition of the Orthogonal Gold sequences is more general, in the sense that the sequence ( $\hat{a}$ ) can be an arbitrary sequence with zero at the right end. In that case the

20 set of sequences ( $\hat{G}$ ) would still be orthogonal but the periodic cross-correlation functions between the sequences from the set will not be optimum.

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An equivalent definition of the set  $(G)$  of Orthogonal Gold sequences with elements belonging to a alphabet of -1 and +1 can be obtained by mapping  $0 \rightarrow 1$  and  $1 \rightarrow -1$ , while the modulo two summation should be replaced by multiplication between row elements of the matrix  $\hat{M}$  and elements of the sequence  $\hat{a}$ .

The usual receiver method for CDMA systems is so-called RAKE combining, which assumes weighted summation of separately despread multipath signal components, with appropriate compensation of corresponding components' delays.

10 In the multicode CDMA systems, the number of RAKE receivers needed for a single mobile-base station connection is equal to the maximum number of parallel code channels. As all RAKEs work in parallel, the baseband receiver consists of multiple despreaders, each corresponding to a different receiving path,  
15 and each consisting of a number of correlators corresponding to the different code channels.

The despread implementation depends on the actual spreading method, i.e. which spreading sequence is used. If the spreading sequence is binary, with +1 and -1 elements, it is called Binary  
20 Phase Shift Keying (BPSK) spreading, while when the spreading method is complex, consisting of real and imaginary binary sequences it is called QuadraPhase Shift Keying (QPSK) spreading. Usually QPSK spreading is performed with different real and imaginary binary sequences.

25 The efficiency of the despread for the multicode CDMA systems directly depends on a bank of correlators corresponding to the binary spreading sequences.

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The maximum number of sequences in any orthogonal set is equal to the sequence length. Therefore, if the length of sequences is  $N$ , it is necessary to perform  $N^2$  operations for each sample at the output of the bank of correlators.

- 5 An implementation of the bank of correlators which provides less than  $N^2$  operations per output sample is an efficient implementation.

Two kinds of orthogonal sets of sequences are proposed for a Multicode DS-CDMA scheme; sets of Walsh sequences and sets of  
10 Orthogonal Gold sequences. A bank of correlators corresponding to a set of Walsh sequences can be efficiently implemented by using a Fast Hadamard Transform (FHT). In that, case the number of operations required for each output sample is  $N \times \log_2 N$ . An implementation of a bank of correlators corresponding to  
15 orthogonal Gold sequences that requires less than  $N^2$  operations is also desirable.

#### SUMMARY OF THE INVENTION

The object of present invention is to solve the problem how to reduce the complexity of a bank of correlators matched to a set  
20 of Orthogonal Gold sequences.

Another problem which the invention is intended to solve is to reduce the power consumption in a receiver when despread receiving radio signals.

In accordance with the present invention, the aforesaid problems  
25 are overcome and resolved by implementing the bank of correlators in a more efficient way compared to a straightforward implementation which uses a correlator for each Orthogonal Gold sequence.

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The implementation of the bank of correlators is performed in the following way. Multiplying a received sequence element-by-element with an arbitrary sequence, that is used for generation of the Orthogonal Gold sequences, as to obtain a set of products. The elements in the set of products are permuted according to a certain rule in order to obtain a permuted sequence which is equal to a sum of a number of sequences from a set of Walsh sequences. A Fast Hadamard Transform is then performed on said permuted sequence as to obtain correlation values between the received sequence and each possible Walsh sequence.

An advantage afforded by the present invention is that the number of operations used when despreading a received sequence are essentially decreased.

A further advantage afforded by the present invention is that due to the smaller number of operations during the despreading of received radio signals a reduction of power consumption is obtained.

Another advantage afforded by the present invention smaller area needed for integrated circuit implementation of the aforementioned bank of correlators.

#### BRIEF DESCRIPTION OF THE DRAWINGS

The invention will now be described in more detail with reference to exemplifying embodiments thereof and also with reference to the accompanying drawings, in which

Figure 1 is a block diagram illustrating a part of a receiver operable to receive a BPSK-spread signal used in a DS-CDMA system.



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Figure 2 is block diagram illustrating part of a receiver operable to receive a QPSK-spread signal used in a DS-CDMA system.

Figure 3 is a block diagram is illustrating background art of a bank of correlators.

Figure 4 is a flowchart of an embodiment of an inventive method for implementing the bank of correlators.

Figure 5 is flowchart of another embodiment of the inventive method for implementing the bank of correlators.

Figure 6 is a block diagram illustrating an embodiment of an inventive arrangement of the bank of correlators.

Figure 7 is a block diagram illustrating another embodiment of the inventive arrangement of the bank of correlators.

#### DETAILED DESCRIPTION

Figure 1 illustrates a part of a receiver 100 used in a multicode DS-CDMA system. In a corresponding transmitter, all N transmitted code channels have been spread with a set of Orthogonal Gold sequences  $G_0, G_1, \dots, G_{N-1}$  respectively, and utilising method called Binary Phase Shift Keying (BPSK). The part of a receiver 100 comprises a quadrature detector 101 which generates an inphase signal on an output 102 and a quadrature signal on an output 103 for radio frequency signals which appear on an input 104. Each output 102, 103 is connected to a branch, an inphase signal branch 112 and a quadrature signal branch 113, that comprises the following entities connected to each other: a lowpass filter 105 with a bandwidth proportional to a chip frequency used in the CDMA system; an A/D-converter 106, a pulse

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matched filter 107 which is the same used in the transmitter for spectrum shaping; a downsampling unit 108 for downsampling to one sample per chip and a bank of correlators 109. Each bank of correlators generates  $N$  correlation values.

5 On a first output 110 of the bank of correlators 109 in the inphase signal branch 112 a first code channel inphase signal is generated and on a first output 111 of the bank of correlators 109 in the quadrature signal branch 113 a first code channel quadrature signal is generated. The two said outputs 110,111 are  
10 combined into a pair of outputs for a first code channel  $CH_1$ . All other outputs of each bank of correlators are combined in the same way to form  $N$  pairs of output which correspond to  $N$  code channels. Each code channel can be combined with the same code channel obtained from another reception path.

15 Figure 2 illustrates part of another receiver 200 used in a multicode CDMA system. In a corresponding transmitter, the  $N$  transmitted code channels have been spread with a set of Orthogonal Gold sequences  $G_0, G_1, \dots, G_{N-1}$  respectively, and utilising method called QuadraPhase Shift Keying (QPSK). This  
20 method produce  $N/2$  code channels such that each code channel is spread with two sequences in the corresponding transmitter. For example, a first code channel  $CH_1$  is spread with a first Orthogonal Gold sequence  $OG_0$  and a second Orthogonal Gold sequence  $OG_1$ .

25 The part of a receiver 200 comprises the same units as the part of a receiver 100 described in conjunction with figure 1. Due to the fact that the used utilising method is QPSK the number of the received code channels are  $N/2$  and the arrangement of the  $N$  outputs of the banks of correlators is different than in the  
30 part of receiver 100.

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A first output 201 of the bank of correlators 109 in the inphase signal branch 112 and a second output 204 of the bank of correlators in the quadrature signal branch 113 are connected to an adder 205, which is operable to adding the output signals on the outputs 201, 204. A sum is obtained as an output signal on an output 207 of the adder 205. The output signal on the output 207 is the inphase signal of the first code channel  $CH_1$ .

A second output 202 of the bank of correlators in the inphase signal branch 112 and a first output 203 of the bank of correlators in the quadrature signal branch 113 are connected to an adder 206, which is operable to adding the output signal on the output 202 to the output signals on the output 203. A sum is obtained as an output signal on an output 208 of the adder 206. The output signal on the output 208 is the quadrature signal of the first code channel  $CH_1$ . All  $N$  outputs from the two banks of correlators are arranged in the same way. This means that the  $N/2$ :th code channel is obtained with two last output of the two banks of correlators. That is, a  $N-1$ :th output of the bank of correlators in the inphase signal branch 112 and a  $N$ :th output of the bank of correlators in the quadrature signal branch 113 are connected in the same way as the output 201 and output 204 and that a  $N$ :th output of the bank of correlators in the inphase signal branch 112 and a  $N-1$ :th output of the bank of correlators in the quadrature signal branch 113 are connected in the same way as the output 211 and output 212.

The explanation for the above arrangements of the outputs of the banks of correlators can be obtained from the following simple relation, wherein the inphase signal to the bank of correlators is denoted  $d_i$  and the quadrature signal is denoted  $d_q$ . The first code channel has been spread with  $OG_0$  and  $OG_1$ . Hence the

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following multiplication has to be performed to obtain the first code channel  $CH_1$ :

$$(d_r + jd_i)(OG_0 - jOG_1) = (d_rOG_0 + d_iOG_1) + j(d_iOG_0 - d_rOG_1).$$

The real part  $(d_rOG_0 + d_iOG_1)$  is what is obtained on the output 207 and the imaginary part  $(d_iOG_0 - d_rOG_1)$  what is obtained on the output 208. The real part and the imaginary part correspond to the inphase signal and the quadrature signal of the first code channel respectively.

The implementation of the bank of correlators does not depend on which spreading method that is used in the transmitter. The code channels are obtained by arranging the outputs of the banks of correlators in a way specific for the applied spreading method.

Figure 3 illustrates the background art for the bank of correlators (109, figure 1 and figure 2) used for despreading multicode channels. A bank of correlators 300 comprises N correlators  $Corr_1, \dots, Corr_N$ , where each correlator generates a correlation signal on output  $out_1, \dots, out_N$ . Each Orthogonal Gold sequence  $OG_0, \dots, OG_{N-1}$ , used for spreading the code channels in the transmitter, is stored in registers  $ref_1, \dots, ref_N$ . For example, in register  $ref_1$  is the Orthogonal Gold sequence  $OG_0$  stored and in register  $ref_N$  is the Orthogonal Gold sequence  $OG_{N-1}$  stored. A sampled signal received from the downsampling unit 108 is connected to an input 201 of the bank of correlators 109. Each sample is shifted into all registers  $reg_1, \dots, reg_N$  and a multiplication is performed continuously bit-by-bit after every shift with the stored Orthogonal Gold sequence. Results of each bit multiplication are summed in summarizer S and the sum is output on the output  $out_1, \dots, out_N$  respectively as a correlation

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value between the Orthogonal Gold sequence and the received signal. For example, if the Orthogonal Gold sequence  $OG_0$  is stored in the register  $ref_1$ , the bank of correlators is connected to the downsampling unit in the inphase signal branch 112, and the BPSK spreading method is used, the correlation signal on the output  $out_1$  corresponds to the inphase signal of the code channel  $CH_1$ . With the same conditions stated above, except that the bank of correlators is connected to the quadrature signal branch 113, the quadrature signal for code channel  $CH_1$  is obtained.

This implementation of the bank of correlators is inefficient. To despread all code channels it takes  $N^2$  operations, which is time- and powerconsuming.

A flowchart, figure 4, illustrates an embodiment of an inventive method concerning how to implement an efficient bank of correlators used in a receiver in a multicode CDMA system. In a first step 401  $N$  input samples  $SS = s_0, s_1, \dots, s_{N-1}$  are buffered. In a second step 402 the input samples  $s_0, s_1, \dots, s_{N-1}$  are multiplied element-by-element with a arbitrary sequence  $\hat{a} = a_0, a_1, \dots, a_{N-1}$ , therewith a sequence  $\hat{p} = p_0, p_1, \dots, p_{N-1} = s_0 * a_0, s_1 * a_1, \dots, s_{N-1} * a_{N-1}$  is obtained. The sequence  $\hat{a}$  is the same used when the Orthogonal Gold sequences  $\hat{G}$  are generated in the transmitter. The obtained sequence  $\hat{p}$  is then, in a third step 403, permuted in a certain way, which will be described in more detail below, in order to obtain a sequences  $\hat{w}$ , which is equal to a sum of a number of sequences from a set of so-called Walsh sequences. The sequence  $\hat{w}$  is, in a step 404, transformed with a Fast Hadamard Transform as to obtain a set of correlation values  $\hat{C} = CV_0, \dots, CV_{N-1}$  between said sequence  $\hat{w}$  and each of  $N$  Walsh

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sequences. By using this inventive method for an inphase signal and for a quadrature signal it will be possible as earlier described to efficiently despread code channels used in a multicode channel system.

5 A flowchart, figure 5, illustrates another embodiment of the inventive method. In a first step 501, the  $N$  input samples  $SS = s_0, s_1, \dots, s_{N-1}$  are buffered. A permutation is carried out in a certain way, described in detail below, on the buffered input samples  $SS$  in a second step 501, in order to obtain a permuted version  $SS_p = s_{p0}, s_{p1}, \dots, s_{pN-1}$  of the input sequence  $SS$ . In a third step 503 the permuted version of all  $N$  input samples  $s_{p0}, s_{p1}, \dots, s_{pN-1}$  are multiplied element-by-element with a permuted version  $\hat{a}_p = a_{p0}, a_{p1}, \dots, a_{pN-1}$  of the sequence  $\hat{a} = a_0, a_1, \dots, a_{N-1}$ , therewith sequence  $\hat{w} = w_0, w_1, \dots, w_{N-1}$  is obtained. The sequence  $\hat{a}$  is permuted in a certain way, described below, before it is used. The sequence  $\hat{w}$  is then, in a fourth step 504, transformed with the Fast Hadamard Transform as to obtain correlation values  $CV_0, \dots, CV_{N-1}$  between said sequence  $\hat{w}$  and each of  $N$  Walsh sequences.

The permutation rule used in present invention is one which maps Orthogonal Gold codes into Walsh sequences. For each matrix  $\hat{M}$  there are many column permutations which produce the same set of Walsh sequences, but with different ordering of Walsh sequences. All these permutations are related to the natural state sequence of the appropriate linear-feedback shift register sequence generator, or equivalently, to the sequence of Galois Field  $GF(2^n)$  elements  $1, \alpha, \alpha^2, \dots, \alpha^{n-2}$ , where  $\alpha$  is a primitive element

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of  $GF(2^n)$ . The determination of the column permutation of matrix  $\hat{M}$  will be illustrated by the following example.

If  $N=2^n=8$ , the m-sequence  $\{x_k\} = [1 \ 1 \ 1 \ 0 \ 0 \ 1 \ 0]$ ,

can be used to generate the matrix  $\hat{M}$  given by:

$$5 \quad \hat{M} = \begin{bmatrix} 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 1 & 0 & 0 \\ 1 & 1 & 0 & 0 & 1 & 0 & 1 & 0 \\ 1 & 0 & 0 & 1 & 0 & 1 & 1 & 0 \\ 0 & 0 & 1 & 0 & 1 & 1 & 1 & 0 \\ 0 & 1 & 0 & 1 & 1 & 1 & 0 & 0 \\ 1 & 0 & 1 & 1 & 1 & 0 & 0 & 0 \\ 0 & 1 & 1 & 1 & 0 & 0 & 1 & 0 \end{bmatrix}.$$

The m-sequence  $\{x_k\}$  is generated by a recursion:  
 $x_{k+3} = x_{k+1} + x_k \pmod{2}$ , which is obtained from the primitive  
 polynomial:  $f(x) = x^3 + x + 1$  over  $GF(2)$ . The initial three values for  
 the recursion, or equivalently, the initial state of the linear-  
 10 feedback shift register sequence generator, are taken to be:  
 $[x_1 \ x_2 \ x_3] = [1 \ 1 \ 1]$ .

The primitive polynomial defines the extended Galois Field  
 $GF(2^3)$  whose elements ordered into the sequence of the  
 successive powers of a primitive element  $\alpha$  are given in the  
 15 following table 1.

table 1.

Field elements	Decimal value	Position	Basic Permutation (of position)	Permuted Decimal Values
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$[0 \ 0 \ 1] = \alpha^0$	1	1	8	0
$[0 \ 1 \ 0] = \alpha^1$	2	2	1	1
$[1 \ 0 \ 0] = \alpha^2$	4	3	2	2
$[0 \ 1 \ 1] = \alpha^3$	3	4	4	3
$[1 \ 1 \ 0] = \alpha^4$	6	5	3	4
$[1 \ 1 \ 1] = \alpha^5$	7	6	7	5
$[1 \ 0 \ 1] = \alpha^6$	5	7	5	6
$[0 \ 0 \ 0]$	0	8	6	7

The basic column permutation which maps M-transform matrix into the matrix whose rows are different Walsh functions is obtained by reordering the sequence of Galois field elements so that their decimal values are linearly ranging from 0 to 7.

- 5 Reordering the positions of columns of matrix  $\hat{M}$  according to the basic permutation  $p=[8 \ 1 \ 2 \ 4 \ 3 \ 7 \ 5 \ 6]$  produces the new matrix given in table 2. The rows of this matrix are Walsh functions, so by suitable permutation of rows this matrix can be transformed onto Sylvester-type Hadamard matrix. The Sylvester-
- 10 type Hadamard matrix H of order 8 is given in table 3. The rows of H are Walsh sequences designated as W0 to W7.

table 2.

Permuted Matrix $\hat{M}$	Walsh Function
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0 0 0 0 0 0 0 0	=W0
0 1 1 0 1 0 0 1	=W7
0 1 1 0 0 1 1 0	=W3
0 1 0 1 0 1 0 1	=W1
0 0 0 0 1 1 1 1	=W4
0 0 1 1 0 0 1 1	=W2
0 1 0 1 1 0 1 0	=W5
0 0 1 1 1 1 0 0	=W6

table 3.

Sylvester-type Hadamard Matrix	Walsh Function
0 0 0 0 0 0 0 0	=W0
0 1 0 1 0 1 0 1	=W1
0 0 1 1 0 0 1 1	=W2
0 1 1 0 0 1 1 0	=W3
0 0 0 0 1 1 1 1	=W4
0 1 0 1 1 0 1 0	=W5
0 0 1 1 1 1 0 0	=W6

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0 1 1 0 1 0 0 1	=W7
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Besides the basic permutation, there are many other permutations which will produce the same set of Walsh functions. They can be obtained by applying the same principle from the previous example to the transformed matrix of  $GF(2^n)$  elements, obtained by any combination of the following operations: columns permutation of field elements, and cyclic shift of  $1, \alpha, \alpha^2, \dots, \alpha^{n-2}$  sequence. The total number of permutations which can be obtained in this way, for the Orthogonal Gold sequences of length  $N=2^n$ , is  $M = n! \times (N - 1)$ .

However, the total number of permutations is even larger, what can be seen if the transformed matrix of  $GF(2^n)$  elements is defined most generally. Namely, the rotation of any column corresponding to the nonzero  $GF(2^n)$  elements in the field elements matrix, that produces all different decimal values, will result in a additional permutation for matrix  $\hat{M}$ . In that way, for example, it can be obtained 168 permutations for  $n=3$ , which is 4 times larger than  $3! \times (8 - 1)$ . For  $n=4$ , it can be found  $56 \times 4! \times (16 - 1)$  permutations.

The following example illustrates permutation construction using transformed matrix of Galois Field elements. If the first and second column of the field elements in table 1 have exchanged the position, another permutation can be obtained as shown in table 4.

table 4.

Field	Decimal	Position	Basic Permutation	Permuted
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elements	value		(of position)	Decimal Values
$[0 \ 0 \ 1] = \alpha^0$	1	1	8	0
$[1 \ 0 \ 0] = \alpha^2$	4	2	1	1
$[0 \ 1 \ 0] = \alpha^1$	2	3	3	2
$[1 \ 0 \ 1] = \alpha^6$	5	4	7	3
$[1 \ 1 \ 0] = \alpha^4$	6	5	2	4
$[1 \ 1 \ 1] = \alpha^5$	7	6	4	5
$[0 \ 1 \ 1] = \alpha^3$	3	7	5	6
$[0 \ 0 \ 0]$	0	8	6	7

The permutation when applied to the matrix  $\hat{M}$  will produce another matrix containing the set of Walsh functions, as shown in table 5.

5 table 5.

Permuted Matrix $\hat{M}$	Walsh Function
0 0 0 0 0 0 0 0	=W0
0 1 1 0 1 0 0 1	=W7
0 1 0 1 1 0 1 0	=W5

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0 1 0 1 0 1 0 1	=W1
0 0 1 1 0 0 1 1	=W2
0 0 0 0 1 1 1 1	=W4
0 1 1 0 0 1 1 0	=W3
0 0 1 1 1 1 0 0	=W6

Figure 6 illustrates an embodiment of an inventive arrangement of the efficient bank of correlators 600 used in a receiver in a multicode CDMA system. A first register 602 converts serially received, on an input 601, input samples  $SS = s_0, s_1, \dots, s_{N-1}$  to parallel. N serially received input samples SS are output on N parallel output  $o_0, \dots, o_{N-1}$  to a multiplier 603. The arbitrary sequence  $\hat{a} = a_0, a_1, \dots, a_{N-1}$  is stored in a memory 605 which has connections  $c_0, \dots, c_{N-1}$  to the multiplier 603. Each element  $a_0, a_1, \dots, a_{N-1}$  is output on a corresponding connection  $c_0, \dots, c_{N-1}$  and multiplied in the multiplier 603 with a corresponding input sample  $s_0, s_1, \dots, s_{N-1}$ , therewith the product  $\hat{p} = p_0, p_1, \dots, p_{N-1} = s_0 * a_0, s_1 * a_1, \dots, s_{N-1} * a_{N-1}$  is obtained. Output  $\hat{u} = u_0, u_1, \dots, u_{N-1}$  of the multiplier are connected to a second register 604 in which the permutation is performed. The obtained permuted version of the product  $\hat{p}$  is the sequence  $\hat{w}$ , which is equal to the sum of a number of sequences of the so-called Walsh sequences. The elements  $w_0, w_1, \dots, w_{N-1}$  of the sequence  $\hat{w}$  are, via a corresponding connection  $v_0, v_1, \dots, v_{N-1}$ , connected to a bank of Walsh correlators 606, implemented as Fast Hadamard Transformer,

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which generates on output  $k_0, k_1, \dots, k_{N-1}$  the correlation values  $CV_0, \dots, CV_{N-1}$  corresponding to each Walsh sequence  $w_0, w_1, \dots, w_{N-1}$ .

Figure 7 illustrates another embodiment of the inventive arrangement 700. A first register 702 converts serially received, on an input 701, input samples  $SS = s_0, s_1, \dots, s_{N-1}$  to parallel. The register 702 is also arranged so that N serially received input samples SS are permuted and output on N parallel output  $o_0, \dots, o_{N-1}$  to a multiplier 703. The permuted arbitrary sequence  $\hat{a}_p = a_{p0}, a_{p1}, \dots, a_{pN-1}$  is stored in a memory 605 which for each N elements has a connection  $c_0, \dots, c_{N-1}$  to the multiplier 703. Each element  $a_{p0}, a_{p1}, \dots, a_{pN-1}$  is output on the corresponding connection  $c_0, \dots, c_{N-1}$  and multiplied in the multiplier 703 with a corresponding input sample  $s_{p0}, s_{p1}, \dots, s_{pN-1}$ , therewith the products  $s_{p0} * a_{p0}, s_{p1} * a_{p1}, \dots, s_{pN-1} * a_{pN-1}$  are obtained, which form a sequence, which is the sum of a number of sequences, from the set of Walsh sequences. Outputs  $u_0, u_1, \dots, u_{N-1}$  of the multiplier 703 are connected to a bank of Walsh correlators 704, implemented as Fast Hadamard Transformer. The bank of Walsh correlators 704 comprises a buffer for buffering said products  $s_{p0} * a_{p0}, s_{p1} * a_{p1}, \dots, s_{pN-1} * a_{pN-1}$  before the Fast Hadamard Transformation is performed on said products. For the sequence  $s_{p0} * a_{p0}, s_{p1} * a_{p1}, \dots, s_{pN-1} * a_{pN-1}$  the bank of Walsh correlators 704 generates N correlation values  $CV_0, \dots, CV_{N-1}$  corresponding to each of N Walsh sequences.

The mentioned Fast Hadamard Transform, which is known by a person skilled in the art, is a method to increase the efficiency of Hadamard transform. The Hadamard transform is characterised by a Hadamard matrix, in which each row of the Hadamard matrix is orthogonal to every other row of the matrix.

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Each row of the Hadamard matrix can be referred to as a Walsh sequence.

A Hadamard matrix of order  $n$  can be defined recursively as follows:

$$H_{2n} = \begin{bmatrix} H_n & H_n \\ H_n & -H_n \end{bmatrix}.$$

- 5 The Fast Hadamard transform exploits the fact that Hadamard matrices are real, symmetric and row-wise orthogonal. Since the Hadamard matrices contain only  $\pm 1$  values, no multiplications are required in the transform calculation. Moreover the number of additions and subtractions required can be reduced from  $n^2$  to
- 10  $n \times \log_2 n$ , due to the fact that a Hadamard matrix of order  $n$  ( $H_n$ ) can be written as a product of  $n$  sparse matrices, that is,

$$H_n = \tilde{H}_n^{\log_2(n)}$$

where

$$\tilde{H}_n = \begin{bmatrix} 1 & 1 & 0 & 0 & \dots & \dots & \dots & \dots \\ 0 & 0 & 1 & 1 & \dots & \dots & \dots & \dots \\ \dots & \dots & \dots & \dots & \dots & \dots & \dots & \dots \\ \dots & \dots & \dots & \dots & \dots & \dots & \dots & \dots \\ 0 & 0 & \dots & \dots & \dots & \dots & 1 & 1 \\ 1 & -1 & 0 & 0 & \dots & \dots & \dots & \dots \\ 0 & 0 & 1 & -1 & \dots & \dots & \dots & \dots \\ \dots & \dots & \dots & \dots & \dots & \dots & \dots & \dots \\ \dots & \dots & \dots & \dots & \dots & \dots & \dots & \dots \\ \dots & \dots & \dots & \dots & \dots & \dots & 1 & -1 \end{bmatrix}.$$

- 15 Noting that the top  $n/2$  rows and the bottom  $n/2$  rows contain only two nonzero terms per row, the transformation
- $$v = \tilde{H}_n^{\log_2(n)} \times \hat{u} = \tilde{H}_n \times \tilde{H}_n \dots \tilde{H}_n \times \hat{u}$$
- can be accomplished by operating

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$\tilde{H}_n \times \log_2 n$  times on  $u$ , where  $u$  is a column vector of the input sample.

Due to the structure of  $\tilde{H}_n$  only  $n$  additions or subtractions are required each time  $\tilde{H}_n$  operates on a vector, giving a total of  
5  $n \times \log_2 n$  additions and subtractions.

The inventive method and arrangement described in conjunction with the figures require  $2 \times N + N \times \log_2 N$  operations when despreading  $N$  samples. It is assumed that multiplication as well as permutation are performed in  $N$  operations.

10 The mapping between the ordinal numbers of despread outputs and ordinal numbers of code channels requires in principle an additional permutation of Fast Hadamard Transform outputs. However, the amount of corresponding additional processing power depends of the actual implementation. For example, if  
15 despread code channels are further processed in parallel, the hard-wired connection between despread outputs and the rest of the receiver can be permuted, so no additional processing power is required.

As an example, if  $N=64$ , then 512 ( $O(64)=512$ ) operations are  
20 required. Compared with 4096 ( $64 \times 64$ ) operations required by a straightforward implementation, 8 times reduction of the complexity is obtained.

The earlier described inventive method and arrangement do not have to operate in parallel. They can of course operate serially  
25 but they are then less efficient.

The embodiments described above serve merely as illustration and not as limitation. It will be apparent to one ordinary skill in

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the art that departures may be made from the embodiments described above without departing from the spirit and scope of the invention. Therefore, the invention should not be regarded as being limited to the examples described, but should instead  
5 be regarded as being equal in scope to the following claims.



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## CLAIMS

1. A method of despreading a symbol sequence  $(s_0, s_1, \dots, s_{N-1})$  to obtain a number of correlation values  $(CV_0, \dots, CV_{N-1})$ , where said symbol sequence has been spread by a set of Orthogonal Gold sequences  $(\hat{G})$ , which have been generated by means of an arbitrary sequence  $(a_0, a_1, \dots, a_{N-1})$ ,

characterised in the following steps:

a) multiplying (402) element-by-element of said symbol sequence  $(s_0, s_1, \dots, s_{N-1})$  with said arbitrary sequence  $(a_0, a_1, \dots, a_{N-1})$ ,  
10 in order to obtain a set of products  $(s_0 * a_0, s_1 * a_1, \dots, s_{N-1} * a_{N-1})$ ;

b) permuting (403) said set of products  $(s_0 * a_0, s_1 * a_1, \dots, s_{N-1} * a_{N-1})$  in order to obtain a sequence  $(\hat{w})$  equal to a sum of a number of sequences from a set of Walsh sequences;

c) transforming said sequence  $(\hat{w})$  with a Fast Hadamard Transform  
15 so as to obtain said correlation values  $(CV_0, \dots, CV_{N-1})$ .

2. A method of despreading a symbol sequence  $(s_0, s_1, \dots, s_{N-1})$  to obtain a number of correlation values  $(CV_0, \dots, CV_{N-1})$ , where said symbol sequence has been spread by a set of Orthogonal Gold sequences  $(\hat{G})$ , which have been generated by means of an arbitrary sequence  $(a_0, a_1, \dots, a_{N-1})$ ,

characterised in the following steps:

a) permuting (502) said symbol sequence  $(s_0, s_1, \dots, s_{N-1})$ , according to a certain rule in order to obtain a permuted version of said sequence  $(s_{p0}, s_{p1}, \dots, s_{pN-1})$ ;

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b) permuting said arbitrary sequence  $(a_0, a_1, \dots, a_{N-1})$ , according to the same rule in order to obtain a permuted version of said sequence  $(a_{p0}, a_{p1}, \dots, a_{pN-1})$ ;

5 c) multiplying element-by-element of said permuted version of the symbol sequence  $(s_{p0}, s_{p1}, \dots, s_{pN-1})$  with said permuted version of said arbitrary sequence  $(a_{p0}, a_{p1}, \dots, a_{pN-1})$ , as to obtain a sequence  $(\hat{w})$  equal to a sum of a number of sequences from a set of Walsh sequences;

10 d) transforming said sequence  $(\hat{w})$  with Fast Hadamard Transform as to obtain said correlation values  $(CV_0, \dots, CV_{N-1})$ .

3. A method according to claims 1 and 2,

characterised in that:

15 a serial to parallel conversion is performed on said symbol sequence  $(s_0, s_1, \dots, s_{N-1})$  and that said element-by-element multiplications is performed in parallel.

4. A method according to claims 1 and 2,

characterised in that:

20 a buffering of said symbol sequence  $(s_0, s_1, \dots, s_{N-1})$  is performed and that said element-by-element multiplications is performed serially.

5. A method according to claims 1 and 2,

characterised in that:

said arbitrary sequence  $(a_0, a_1, \dots, a_{N-1})$  is a maximal-length PN sequence.

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6. A method of despreading a symbol sequence  $(s_0, s_1, \dots, s_{N-1})$  as to obtain a number of code channels  $(CH_0, CH_1, \dots, CH_M)$  from an incoming code spread radio signal, which is quadrature detected as to obtain an inphase signal and a quadrature signal,

5 characterised in the following steps:

correlating said inphase signal according to steps in claim 1, therewith to obtain N inphase correlation values;

correlating said quadrature signal according to steps in claim 1, therewith to obtain N quadrature correlation values;

10 arrange said inphase correlation values and said quadrature correlation values according to a corresponding spreading method, therewith to obtain said code channels  $(CH_0, CH_1, \dots, CH_M)$ .

7. A method of despreading a symbol sequence  $(s_0, s_1, \dots, s_{N-1})$  as to obtain a number of code channels  $(CH_0, CH_1, \dots, CH_M)$  from an incoming code spread radio signal, which is quadrature detected as to obtain an inphase signal and a quadrature signal,

15 characterised in the following steps:

correlating said inphase signal according to steps in claim 2, therewith to obtain N inphase correlation values;

20 correlating said quadrature signal according to steps in claim 2, therewith to obtain N quadrature correlation values;

arrange said inphase correlation values and said quadrature correlation values according to a corresponding spreading method, therewith to obtain said code channels  $(CH_0, CH_1, \dots, CH_M)$ .

25 8. A method according to claim 6 and 7,

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characterised in that:

said spreading method is Binary Phase Shift Keying (BPSK), and the number of obtained code channels ( $CH_0, CH_1, \dots, CH_M$ ) is equal to the number of obtained correlation values.

5 9. A method according to claim 6 and 7,

characterised in that:

said spreading method is QuadraPhase Shift Keying (QPSK), and the number of obtained code channels ( $CH_0, CH_1, \dots, CH_M$ ) is equal to half of the number of the obtained correlation values.

10 10. An arrangement of a bank of correlators (600) for despreading a symbol sequence ( $s_0, s_1, \dots, s_{N-1}$ ) to obtain a number of code channels ( $CH_0, CH_1, \dots, CH_{N-1}$ ), where the code channels have been spread by a set of Orthogonal Gold sequences ( $\hat{G}$ ), which have been generated by means of an arbitrary sequence  
15 ( $a_0, a_1, \dots, a_{N-1}$ ),

characterised in that said arrangement is comprising:

means for permuting the symbol sequence ( $s_0, s_1, \dots, s_{N-1}$ ) according to a certain rule, in order to obtain a permuted version of said sequence ( $s_{p0}, s_{p1}, \dots, s_{pN-1}$ );

20 means for permuting the arbitrary sequence ( $a_0, a_1, \dots, a_{N-1}$ ) according to the same rule, in order to obtain a permuted version of said sequence ( $a_{p0}, a_{p1}, \dots, a_{pN-1}$ );

means for multiplying element-by-element of said permuted version of said symbol sequence ( $s_{p0}, s_{p1}, \dots, s_{pN-1}$ ) with said  
25 permuted version of said arbitrary sequence ( $a_{p0}, a_{p1}, \dots, a_{pN-1}$ ) as

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to obtain a sequence  $(\hat{w})$  which is a sum of a number of sequences from a set of Walsh sequences;

at least one bank of Walsh correlators, implemented as Fast Hadamard Transformer, operable to transforming said sequence.

5  $(\hat{w})$  to obtain said correlation values  $(CV_0, \dots, CV_{N-1})$ .

11. An arrangement of a bank of correlators (700) for despreading a symbol sequence  $(s_0, s_1, \dots, s_{N-1})$  to obtain a number of code channels  $(CH_0, CH_1, \dots, CH_{N-1})$ , where the code channels have been spread by a set of Orthogonal Gold sequences  $(\hat{G})$ ,  
10 which have been generated by means of an arbitrary sequence  $(a_0, a_1, \dots, a_{N-1})$ ,

characterised in that said arrangement is comprising:

means for multiplying element-by-element of said symbol sequence  $(s_0, s_1, \dots, s_{N-1})$  with said arbitrary sequence  $(a_0, a_1, \dots, a_{N-1})$ , as  
15 to obtain a set of products  $(s_0 * a_0, s_1 * a_1, \dots, s_{N-1} * a_{N-1})$ ;

means for permuting said set of products  $(s_0 * a_0, s_1 * a_1, \dots, s_{N-1} * a_{N-1})$  according to a certain rule, in order to obtain a sequence  $(\hat{w})$  which is the sum of a number of sequences from a set of Walsh sequences;

20 at least one bank of Walsh correlators, implemented as Fast Hadamard Transformer, operable to transforming said sequences  $(\hat{w})$  to obtain said correlation values  $(CV_0, \dots, CV_{N-1})$ .

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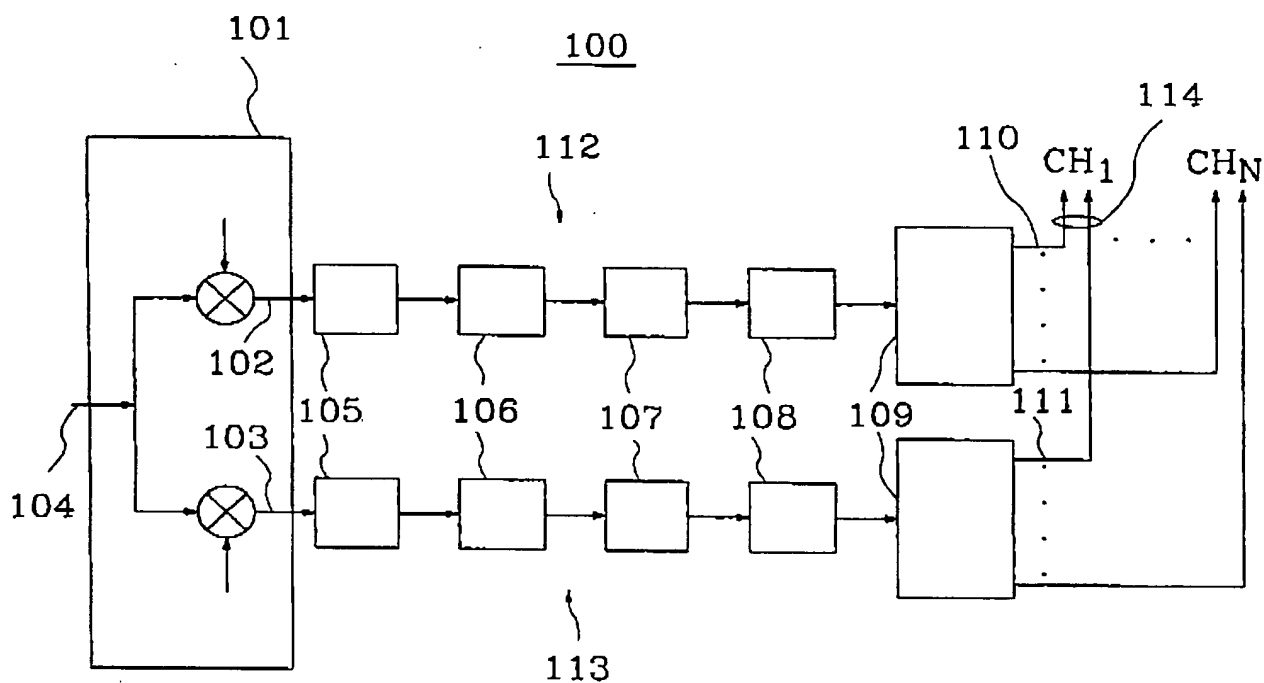


Fig. 1

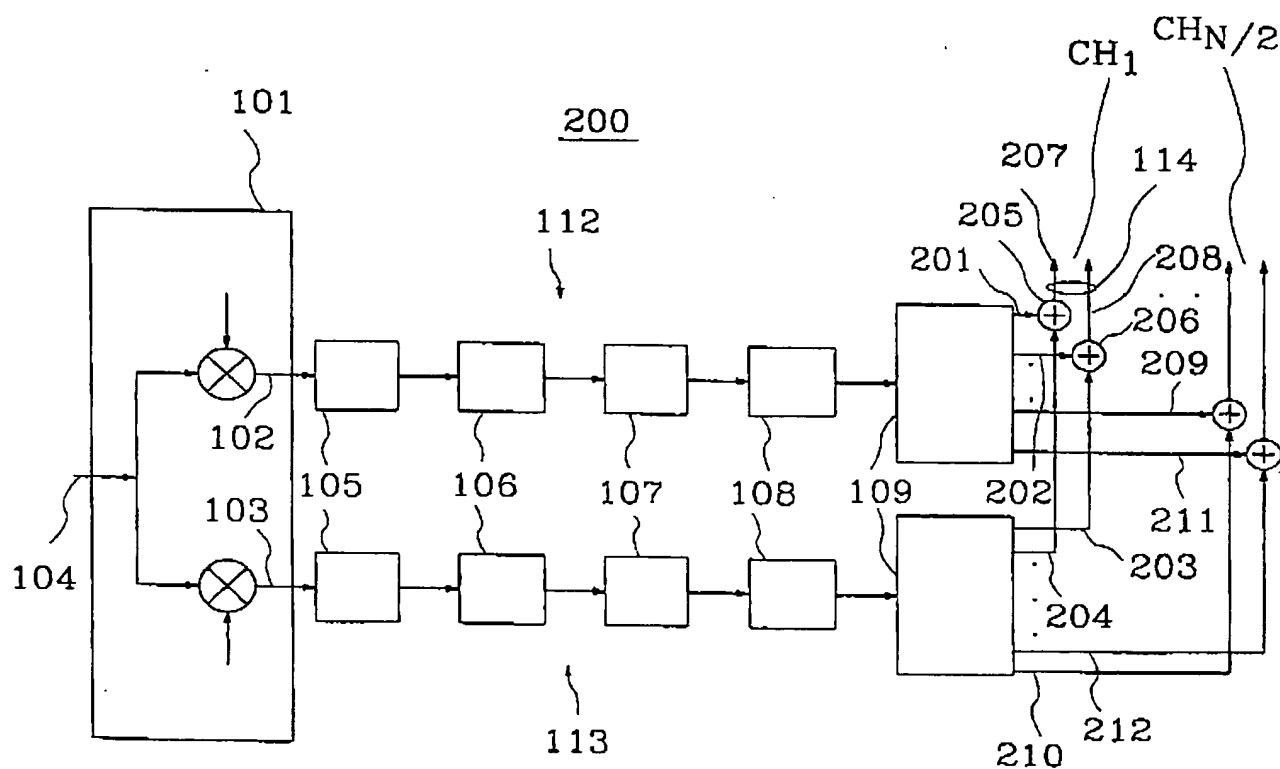


Fig. 2

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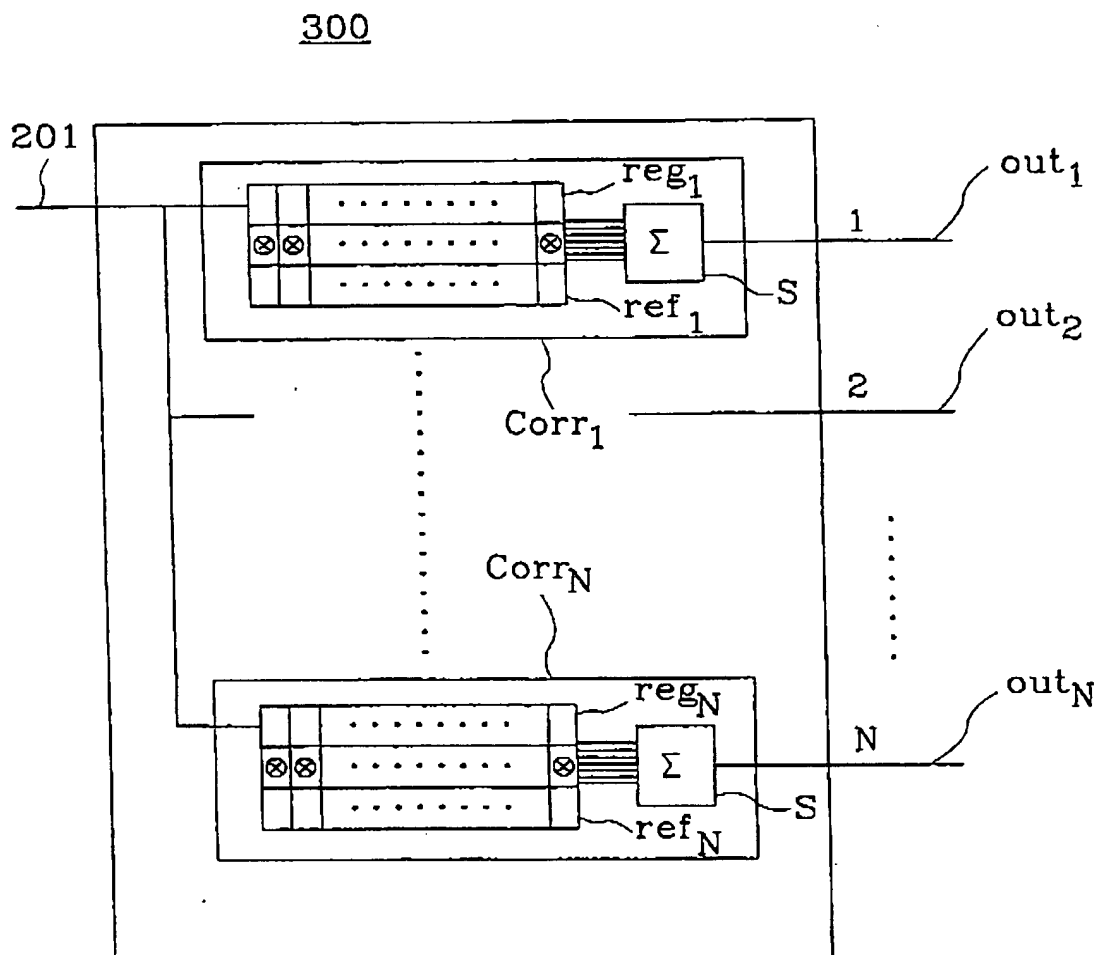


Fig. 3

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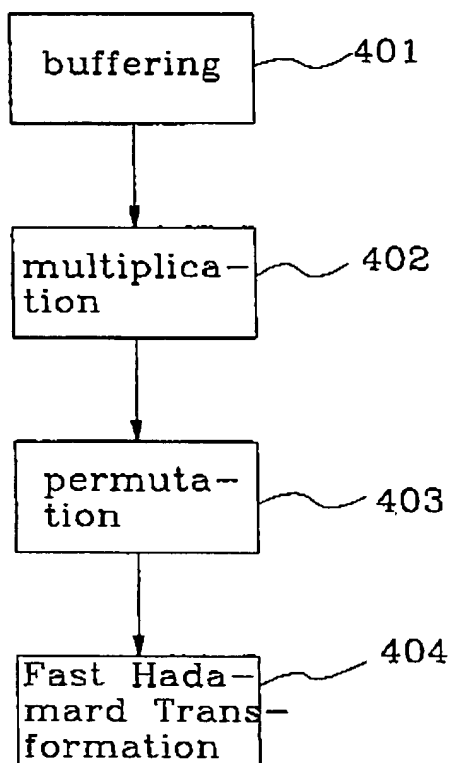


Fig. 4

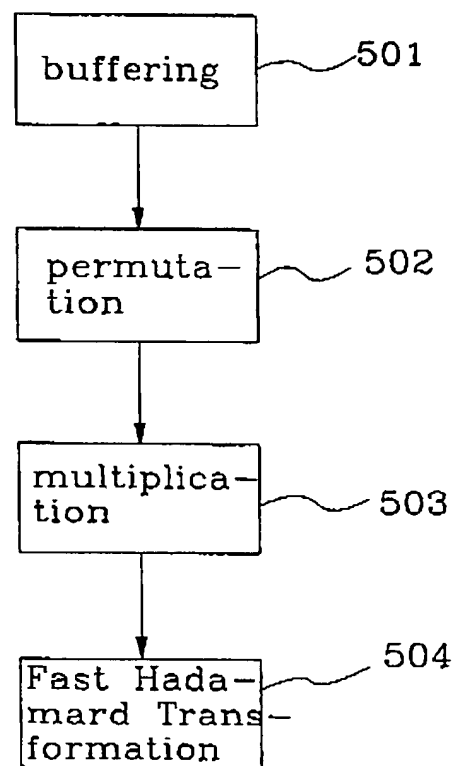


Fig. 5



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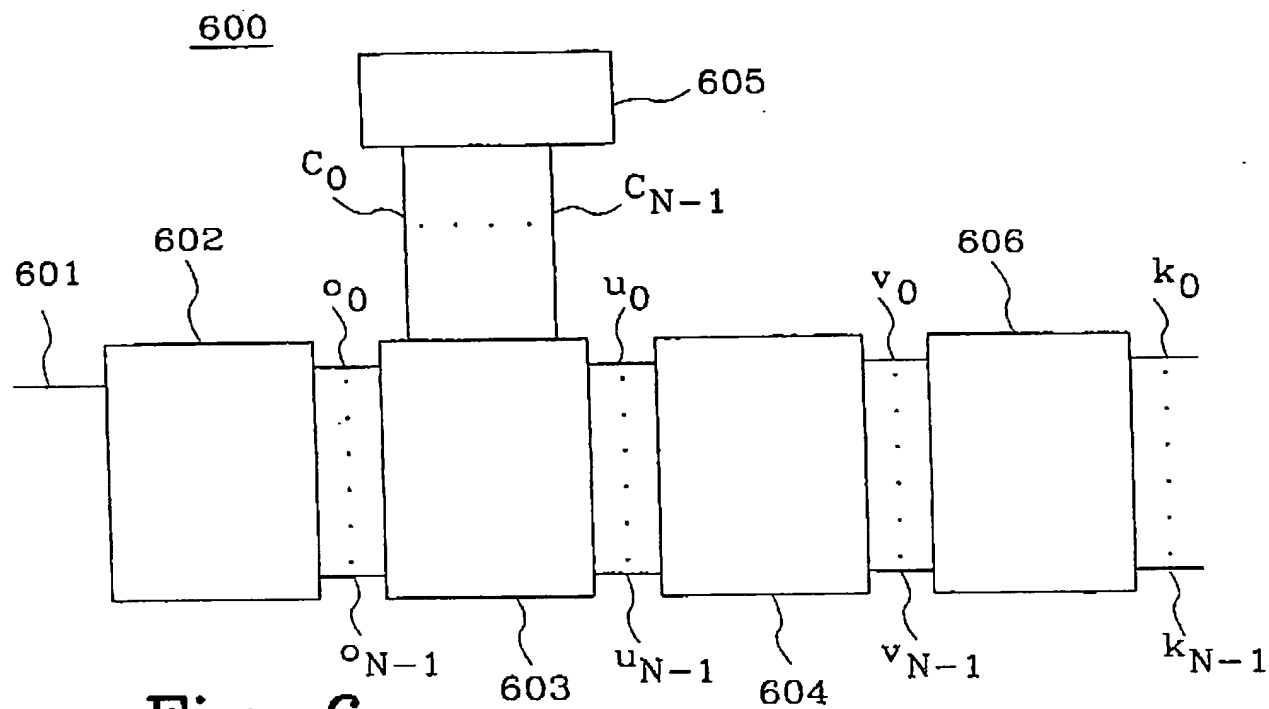


Fig. 6

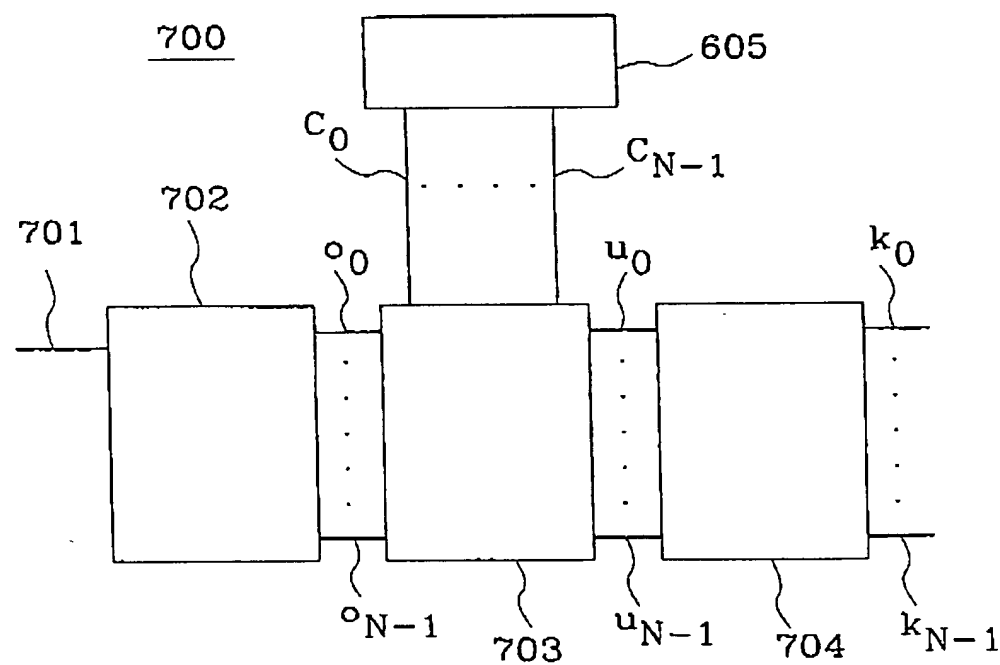


Fig. 7

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## INTERNATIONAL SEARCH REPORT

International application No.

PCT/SE 97/02183

## A. CLASSIFICATION OF SUBJECT MATTER

IPC6: H04J 13/04

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC6: H04Q, H04J

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5463657 A (B.F. RICE), 31 October 1995 (31.10.95), page 3, line 30 - line 41; page 5, line 25 - line 41; page 6, line 39 - line 54, column 7, line 47 - column 8, line 17, column 15, line 5 - line 27	6-11
A	US 5353352 A (P.W. DENT ET AL), 4 October 1994 (04.10.94), column 3, line 1 - line 27; column 7, line 11 - line 30; column 13, line 58 - column 14, line 57, abstract	6-11
A	US 5497395 A (YU-CHEUN JOU), 5 March 1996 (05.03.96), column 2, line 18 - column 4, line 30	6-11

☐ Further documents are listed in the continuation of Box C.☒ See patent family annex.

\* Special categories of cited documents

"A" document defining the general state of the art which is not considered to be of particular relevance

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Date of the actual completion of the international search

Date of mailing of the international search report

11 May 1998

15-05-1998

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## INTERNATIONAL SEARCH REPORT

International application No.  
PCT/SE 97/02183

## Box I Observations where certain claims were found unsearchable (Continuation of Item 1 of first sheet)

This international search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☒ Claims Nos.: 1-5  
because they relate to subject matter not required to be searched by this Authority, namely:  
Mathematical theories (Rule 39), Claim 1-5 concerns symbol sequences and manipulation of symbol sequences.
2. ☐ Claims Nos.:  
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:
3. ☐ Claims Nos.:  
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

## Box II Observations where unity of invention is lacking (Continuation of Item 2 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

1. ☐ As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.
3. ☐ As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:
4. ☐ No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

Remark on Protest

☐  
☐

- The additional search fees were accompanied by the applicant's protest.
- No protest accompanied the payment of additional search fees.

**INTERNATIONAL SEARCH REPORT**  
Information on patent family members

29/04/98

International application No.  
PCT/SE 97/02183

Patent document cited in search report			Publication date	Patent family member(s)	Publication date
US	5463657	A	31/10/95	NONE	
US	5353352	A	04/10/94	AU 665254 B	21/12/95
				AU 4026993 A	18/11/93
				BR 9305479 A	11/10/94
				CA 2110995 A	28/10/93
				EP 0565506 A	13/10/93
				FI 935526 A	14/01/94
				JP 6511371 T	15/12/94
				MX 9301960 A	01/10/93
				NZ 251900 A	28/10/96
				SG 43043 A	17/10/97
				US 5550809 A	27/08/96
				WO 9321709 A	28/10/93
US	5497395	A	05/03/96	NONE	

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